

AMENDMENTS TO THE SPECIFICATION

[0011] Note that the transmission path may be cables or could be wireless, microwave, etc. Backplane BP1 is constructed almost exactly as is backplane BP0, except that secondary clock source 12 is connected to the D input of mux 13-1 instead of to the A input as was primary clock source 11 in backplane BP0. Control 17-0 (and control 17-1) receive primary and secondary clock signals as shown. Since there is no A input to mux 13-1 [[. The]] , the B input from cable 101 is switched to the mux output and to control signal controlled circuits 16-11 to 16-1N. These same signals (the primary clock signal from board BP0) is fed back via cable 102 to the B and C inputs to mux 13-0. This has no effect on the system, since mux 13-0, under control of control 17-0 continues to have the A input (primary signal source) switched to the output of mux 13-0.